L Number	Hits	Search Text	DB	Time stamp
-	1	20020002647.PN.	USPAT; US-PGPUB	2003/10/29 14:41
-	82	"1394" & split & isochronous & acknowledge$	USPAT; US-PGPUB	2003/10/30 14:16
-	82	"1394" & split & isochronous & acknowledge$	USPAT; US-PGPUB	2003/10/30 14:16
-	87	"1394" & split & isochronous & acknowledge$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:44
-	66	split near10 acknowledge$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:26
-	2	"1394" & ((subaction "sub-action" "sub action") adj gap) & (split near10 acknowledge$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:21
-	16	("1394" & split & isochronous & acknowledge$) & ((subaction "sub-action" "sub action") adj gap)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:22
-	3006	(error fault) near10 acknowledge$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:26
-	3	((error fault) near10 acknowledge$) & (("1394" & split & isochronous & acknowledge$) & ((subaction "sub-action" "sub action") adj gap))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:35
-	3	"data error acknowledge"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:36
-	21467	"data error"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:36
-	5	"data error" & "1394" & (("1394" & split & isochronous & acknowledge$) & ((subaction "sub-action" "sub action") adj gap))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:36
-	213	"1394" & split & acknowledge$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:45
-	5	((subaction "sub-action" "sub action") adj gap) & ("1394" & split & acknowledge$) & "data error"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/30 14:45
-	131	(subaction "sub-action" "sub action") adj gap	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 05:59
-	4001	arbitration & acknowledge$ & response	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:00

-	70	"1394" & ((subaction "sub-action" "sub action") adj gap) & (arbitration & acknowledge$ & response)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:01
-	19	"1394" & ((subaction "sub-action" "sub action") adj gap) & (arbitration & acknowledge$ & response) & (retry retries)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:38
-	706	("nv ram" nvram "non-volatile") near5 initiali$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:42
-	20	"1394" & (("nv ram" nvram "non-volatile") near5 initiali$)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:39
-	0	("nv ram" nvram "non-volatile") near10 initiali$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:42
-	0	("nv ram" nvram "non-volatile") near10 initiali$ with 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:42
-	0	("nv ram" nvram "non-volatile" eeprom flash) with initiali$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:43
-	0	("nv ram" nvram "non-volatile" eeprom flash) & initiali$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:49
-	2	initiali$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:43
-	89	initiali$ near10 "1394"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:43
-	89	tree near10 "1394"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:44
-	312586	"nv ram" nvram "non-volatile" eeprom flash	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:44
-	19	(tree near10 "1394") & ("nv ram" nvram "non-volatile" eeprom flash)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:44
-	1	("nv ram" nvram "non-volatile" eeprom flash) near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:50

-	1	("nv ram" nvram "non-volatile" eeprom flash) near15 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:50
-	25	("nv ram" nvram "non-volatile" eeprom flash) & tree & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 06:56
-	12	("nv ram" nvram "non-volatile" eeprom flash rom) & "self-id" & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:01
-	0	("nv ram" nvram "non-volatile" eeprom flash rom) near10 reset$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:02
-	3	("nv ram" nvram "non-volatile" eeprom flash rom) & reset$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:03
-	45	("nv ram" nvram "non-volatile" eeprom flash rom) & reset$ & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:45
-	21	("nv ram" nvram "non-volatile" eeprom flash rom) near10 configur$ & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:24
-	17	"configuration rom" & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:43
-	7	("nv ram" nvram "non-volatile" eeprom flash rom) near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:32
-	3	("nv ram" nvram "non-volatile" eeprom flash rom) & reset$ near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:34
-	39	("nv ram" nvram "non-volatile" eeprom flash rom) & (ieee near5 1394?)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:35
-	1	"configuration rom" near10 isochronous & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:43
-	1	rom near10 "isochronous delay" & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:44
-	1	configuration near10 "isochronous delay" & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:44

-	1	"isochronous delay" & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:44
-	2	rom near15 isochronous & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 07:45
-	32	("nv ram" nvram "non-volatile" eeprom flash rom) & isochronous & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 08:05
-	0	speed near10 configuration near10 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 08:05
-	15	speed near10 configuration & 1394?	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/10/31 08:06

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DOCUMENT-IDENTIFIER: US 20030107987 A1

TITLE: Reconfiguration system for a communication network

---------- KWIC ---------

Abstract Paragraph - ABTX (1):
A reconfiguration system for providing an interconnection capability for an IEEE-1394a or IEEE-1394-2000 based communication network. The reconfiguration system comprises an auxiliary connection system that includes a first port being connectable to a node of a first communication subnetwork and a second port being connectable to a node of a second communication subnetwork. Each of the ports has the capability of establishing or interrupting the sending and receiving of signals compliant with IEEE-1394a or IEEE-1394-2000 standards. A connecting subsystem of the auxiliary connection system relays the signals between the first port and the second port. A port manager system is operatively connected to the first port and the second port for managing the establishing or interrupting of the signals. A connection path is selectively provided between the first and second communication subnetworks to integrate these communication subnetworks into a common network.

Summary of Invention Paragraph - BSTX (7):
[0006] Some, such as the popular IEEE-1394-based bus (viz., IEEE-1394a and IEEE-1394-2000) explicitly impose restrictions against the connection as a "loop" topology. For buses with such restrictions against "loops" or other auxiliary connections, it would normally be necessary and comparatively expensive to implement a complete second, parallel bus, between nodes to gain the desired dual redundancy, as with the prior art alternative networks (e.g., Fibre Channel, Universal Serial Bus, etc.). In fact, the exclusion of the loop as a valid topology for IEEE-1394a and IEEE-1394-2000 based networks offers a unique advantage for those networks for creating a redundant connectivity path with a minimum of extra connectivity wiring (i.e., a single additional reconfigurable link), as compared to those networks which would require duplicating the entire primary network to obtain the same redundant connectivity.

Summary of Invention Paragraph - BSTX (10):
[0008] The present invention is a reconfiguration system for providing an interconnection capability for an IEEE-1394a or IEEE-1394-2000 based communication network. The reconfiguration system comprises an auxiliary connection system that includes a first port being connectable to a node of a first communication subnetwork and a second port being connectable to a node of a second communication subnetwork. Each of the ports has the capability of establishing or interrupting the sending and receiving of signals compliant with IEEE-1394a or IEEE-1394-2000 standards. A connecting subsystem of the auxiliary connection system relays the signals between the first port and the second port. A port manager system is operatively connected to the first port and the second port for managing the establishing or interrupting of the signals. A connection path is selectively provided between the first and second communication subnetworks to integrate these communication subnetworks into a common network.

Detail Description Paragraph - DETX (2):
[0016] Referring to the drawings and the characters of reference marked thereon, FIG. 1 shows the reconfiguration systems of the present invention, designated generally as 10, 10', 10", shown connected in a communication network 12. The communication network 12 is typically a IEEE-1394a or IEEE-1394-2000 based communication network. However, the reconfiguration system may be used with other networks that may benefit from a dynamically connectable auxiliary connection system. The present invention is particularly beneficial for use with a 1394-based system, which prohibits the presence of a loop topology. As will be discussed below in detail, the reconfiguration system 10 of the present invention mitigates the effect of a connectivity fault arising from the loss of a normal connection.

Detail Description Paragraph - DETX (6):
[0020] A second port 24 of the auxiliary connection system is connectable to another node 26 of the second communication subnetwork 16'. Each port has the capability of establishing or interrupting the sending and receiving of signals compliant with IEEE-1394a or IEEE-1394-2000 standards.

Detail Description Paragraph - DETX (9):
[0023] Referring now to FIG. 4, perhaps the most simplistic application of principles of the present invention is illustrated. This is the application of a single configuration system 10 between two nodes 40, 42 of an otherwise completely connected communication network, designated generally as 44. Under normal network operations, this auxiliary link will be disabled, establishing a valid IEEE-1394a or IEEE-1394-2000 topology. In the event of a failure of any of the interconnecting links 46-54, the enabling of the reconfiguration system 10 restores the network to a fully connected operational system.

Detail Description Paragraph - DETX (11):
[0025] Referring now to FIG. 6, the operation of the port manager system is described. The functional block diagram 90 describes the initiation and maintenance of normal bus operations and recovery from a bus segmentation arising from a connection link failure using the features of the present invention. The monitoring of the bus health and enabling and disabling of auxiliary link(s) of the present invention are accomplished by a software-based port manager system residing within each node. The port manager system may be in, for example, a programmable logic device or a dynamically loadable microprocessor, with volatile and/or non-volatile memory portions. Each node maintains knowledge of the topology map of all the nodes in the system, with their respective capabilities. The port manager software is first loaded into each node, 92, whereafter the complete bus startup is initiated, with auxiliary links enabled 94. Doing so will create a loop configuration between some or all of the network nodes, representing an invalid configuration for IEEE-1394a or IEEE-1394-2000 based buses.

Detail Description Paragraph - DETX (12):
[0026] The presence of at least one such loop will subsequently be confirmed 96 by the failure of the bus to complete its self-identification process as evidenced by time-outs within the software, which monitors the progress through a bus reset. This step confirms the presence of at least one such functional auxiliary link. The port manager software, loaded with the preferred loop topology, selects 98 the auxiliary link to be disabled to establish a valid bus topology. Subsequently, it issues commands necessary to disable at least one end of the identified auxiliary link 100, and issues and performs a bus reset 102.

Detail Description Paragraph - DETX (13):
[0027] Following the bus reset, the port manager looks for a satisfactory completion of the bus self-identification process 104. If satisfactory self-ID has been achieved at decision point 106, the bus enters into normal operations at step 110. Otherwise, it enters a start-up diagnostic process 108. At step 110, the port manager initiates a monitoring function that confirms the continued connectivity of the full bus. This is accomplished by maintaining a periodic software handshake between all nodes, which is monitored simultaneously by the port manager software within all nodes on the bus. The presence or absence of the required handshakes is monitored to direct the flow of the software monitoring and recovery processes 112.

Detail Description Paragraph - DETX (14):
[0028] If and when any of the required handshakes fails to be maintained within an established monitoring interval, the software is directed to a link recovery process, which begins at step 114. The first step of the link recovery process is to disable, step 114, one or both ends of link which has been determined to be faulty, using software only, or dedicated hardware switches implemented to perform such enabling/disabling functions under the direction of software. The port manager software initiates the enabling of a new link, step 116, then initiates and performs another bus reset, step 118. The port manager software then determines whether the desired (e.g., full) bus connectivity has been restored 120. If it has, then control is returned to step 110 without any further software action to continue to maintain handshake

connectivity monitoring between all nodes. If the reconfiguration of the bus with the auxiliary link enabled failed to reestablish the desired connectivity, then it shall be presumed that replaced link was probably good. In that case, control is passed to step 122 where the original link configuration is restored and then control is returned back to step 110 for further monitoring. The steps of 110 through 120 or 110 through 122 will continuously be cycled as necessary to maintain a satisfactory link configuration.

Claims Text - CLTX (1):
1. A reconfiguration system for providing an interconnection capability for an IEEE-1394a or IEEE-1394-2000 based communication network, comprising: an auxiliary connection system, comprising: a) a first port being connectable to a node of a first communication subnetwork; b) a second port being connectable to a node of a second communication subnetwork, each said port having the capability of establishing or interrupting the sending and receiving of signals compliant with IEEE-1394a or IEEE-1394-2000 standards; c) a connecting subsystem for relaying said signals between said first port and said second port; and d) a port manager system operatively connected to said first port and said second port for managing said establishing or interrupting of said signals, wherein a connection path is selectively provided between said first and second communication subnetworks to integrate these communication subnetworks into a common network.

Claims Text - CLTX (2):
2. The reconfiguration system of claim 1, wherein said auxiliary communication system comprises means for connecting two communication subnetworks that were previously connected by an operative IEEE-1394a or IEEE-1394-2000 connection that is no longer operative.

Claims Text - CLTX (8):
8. The reconfiguration system of claim 7, wherein said connecting subsystem further comprises a converter connected to said bi-directional wireless communication link for producing IEEE-1394a or IEEE-1394-2000 compliant electrical signals.

Claims Text - CLTX (10):
10. The reconfiguration system of claim 9, wherein said connecting subsystem further comprises a converter connected to said bi-directional photonic communication link for producing IEEE-1394a or IEEE-1394-2000 compliant electrical signals.

Claims Text - CLTX (12):
12. The reconfiguration system of claim 11, wherein said link recovery process, comprises: a) disabling a port of a link that has been determined to be faulty; b) enabling a new link; c) initiating and performing a bus reset; and, d) determining whether bus connectivity has been restored.

Claims Text - CLTX (15):
15. A method for providing an interconnection capability for an IEEE-1394a or IEEE-1394-2000 based communication network, comprising the steps of: a) providing two IEEE-1394a or IEEE-1394-2000 communication subnetworks; b) inserting an auxiliary connection system between one node of each said communication subnetwork, said auxiliary connection system being enableable under desired reconfiguration conditions; and c) enabling said auxiliary connection system, under said desired reconfiguration conditions, to provide a connection path, wherein said two subnetworks are thereby integrated into a common network.

Claims Text - CLTX (16):
16. The method claim 15, wherein said step of inserting an auxiliary connection system comprises connecting two communication subnetworks that were previously connected by an operative IEEE-1394a or. IEEE-1394-2000 connection that is no longer operative.

Claims Text - CLTX (19):

19. The method claim 18, wherein said link recovery process comprises the steps of: a) disabling a port of a link that has been determined to be faulty; b) enabling a new link; c) initiating and performing a bus reset; and, d) determining whether bus connectivity has been restored.

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[PPT] Birds of a Feather on 1394 Interoperability
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